

LDMOS RF Line Power FET Transistor 30 W, 1600-1700 MHz, 28V

Discontinued (For Reference Only)

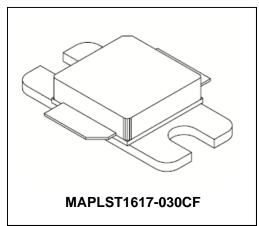
Designed for INMARSAT applications in the 1620-1670 MHz frequency band.

Typical two tone performance (IMD=-30 dBc):

Average output power: 15W Gain: 14dB (typ.) Efficiency: 38% (typ.)

10:1 VSWR ruggedness at 30W, 28V,1670MHz)

Product Image



MAXIMUM RATINGS

Parameter	Symbol	Rating	Units
Drain—Source Voltage	V_{DSS}	65	V_{dc}
Gate—Source Voltage	V_{GS}	20	V_{dc}
Total Power Dissipation @ T _C = 25 °C	P₀	97	W
Storage Temperature	T _{STG}	-40 to +150	°C
Junction Temperature	TJ	+200	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{⊚JC}	1.8	°C/W

NOTE—**CAUTION**—MOS devices are susceptible to damage from electrostatic charge. Precautions in handling and packaging MOS devices should be observed.

[•] Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300

Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298
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Characteristic	Symbol	Min	Тур	Max	Unit	
DC CHARACTERISTICS @ 25°C						
Drain-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 20 μAdc)	V _{(BR)DSS}	65	_	_	Vdc	
Zero Gate Voltage Drain Leakage Current (V _{DS} = 28 Vdc, V _{GS} = 0)	I _{DSS}	_	_	1	μAdc	
Gate—Source Leakage Current (V _{GS} = 5 Vdc, V _{DS} = 0)	I _{GSS}	_	_	1	μAdc	
Gate Threshold Voltage (V _{DS} = 10 Vdc, I _D = 1 mA)	$V_{GS(th)}$	2	_	4	Vdc	
Gate Quiescent Voltage (V _{DS} = 28 Vdc, I _D = 250 mA)	$V_{DS(Q)}$	2	_	4.5	Vdc	
Drain-Source On-Voltage (V _{GS} = 10 Vdc, I _D = 1 A)	$V_{DS(on)}$	-	0.2	-	Vdc	
Forward Transconductance (V _{GS} = 10 Vdc, I _D = 1 A)	Gm	-	1.2	-	S	
DYNAMIC CHARACTERISTICS @ 25°C						
Input Capacitance (Including Input Matching Capacitor in Package) (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{iss}	_	90	_	pF	
Output Capacitance (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{oss}	_	32.5	_	pF	
Reverse Transfer Capacitance (V _{DS} = 28 Vdc, V _{GS} = 0, f = 1 MHz)	C _{rss}	_	1.5	_	pF	
RF FUNCTIONAL TESTS @ 25°C (In M/A-COM Test Fixture)						
CW Gain (V _{DS} = 28 Vdc, Р _{о∪т} = 30 W (avg.), I _{DQ} = 250 mA, f0 = 1670 MHz)	G _{ps}	-	14	-	dB	
CW Drain Efficiency (V _{DS} = 28 Vdc, P _{OUT} = 30 W (avg.), I _{DQ} = 250 mA, f0 = 1670 MHz)	EFF (ŋ)	_	50	_	%	
CW Input Return Loss (V _{DS} = 28 Vdc, P _{OUT} = 30 W (avg.), I _{DQ} = 250 mA, f0 = 1670 MHz)	IRL	_	-10	-9	dB	
IMD (V _{DS} = 28 Vdc, P _{OUT} = 15 W (avg.) (30 W PEP), I _{DQ} = 250 mA, f0 = 1670 MHz, f1 = 1670.1 MHz)	IMD	_	-30	_	dBc	
Output VSWR Tolerance (V _{DS} = 28 Vdc, P _{OUT} = 30 W (avg.), I _{DQ} = 250 mA, f0 = 1670 MHz)	Ψ	No Degradation In Output Power Before and After Test				

⁽¹⁾ Device specifications obtained on a Production Test Fixture.

and/or prototype measurements. Commitment to develop is not guaranteed.

PRELIMINARY: Data Sheets contain information regarding a product M/A-COM Technology

Solutions has under development. Performance is based on engineering tests. Specifications are
typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available.

Commitment to produce in volume is not guaranteed.

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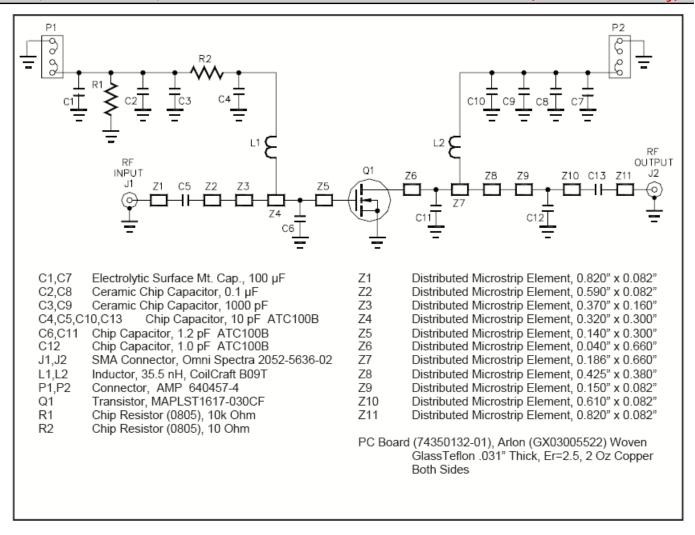


FIGURE 1. 1620—1670 MHZ TEST FIXTURE SCHEMATIC

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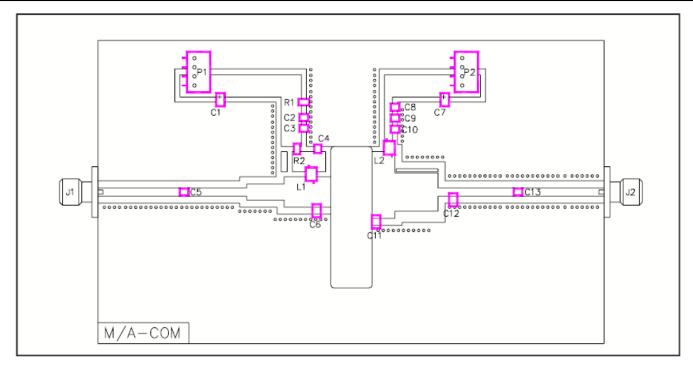


FIGURE 2. 1620—1670 MHZ TEST FIXTURE COMPONENT LAYOUT

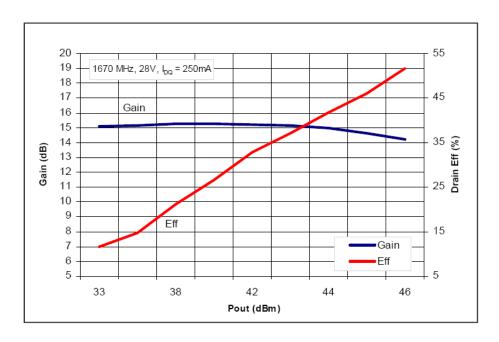
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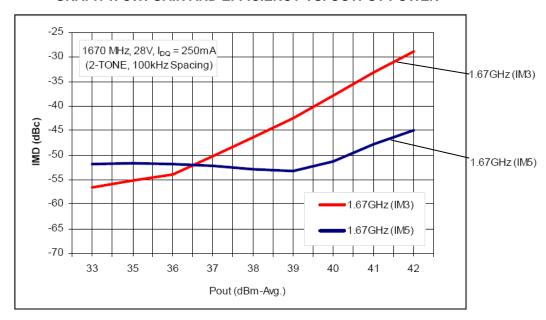


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GRAPH 1. CW: GAIN AND EFFICIENCY VS. OUTPUT POWER



GRAPH 2. TWO TONE: INTERMODULATION DISTORTION VS. OUTPUT POWER

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PACKAGE DIMENSIONS

